Case No. 52669 CIP1



MS PATENT APPLICATION **COMMISSIONER OF PATENTS** P.O. BOX 1450 **ALEXANDRIA, VA 22313-1450**

Transmitted herewith for filing is the patent application of:

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" **SERVICE UNDER 37 CFR 1.10 ON THE DATE** INDICATED BELOW AND IS ADDRESSED TO: MS PATENT APPLICATION, COMMISSIONER OF PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-

EXPRESS MAIL NO: EV 322684530 US
DATE OF DEPOSIT: July 1, 2003
NAME: Justin Goree
SIGNATURE: Justin Horu

inventors:

Manoj KUMAR

Sunil Chandra KASANYAL

For:

A DIGITAL ELECTRONIC CIRCUIT FOR TRANSLATING HIGH VOLTAGE LEVELS TO

LOW VOLTAGE LEVELS

Enclos dare:

Patent Application: 20 pages, 12 claims.

14 sheets of drawings.

The suggested drawing figure to be published is FIG. 4.

A Preliminary Amendment (with 14 replacement drawing sheets).

Assignee info:

Name:

STMicroelectronics Pvt. Ltd. Plot No. 2 & 3, Sector 16A, Institutional Area, Noida-201 3001, Uttar Pradesh, India Address:

Country of Incorporation: India
Applicant claims priority benefit to the following foreign application(s): [X]

India Country:

Application No.: 706/Del/2002 Filing Date: July 1, 2002

and

Country: India

Application No.: 635/Del/2002 Filing Date: June 13, 2002 June 13, 2002

[X] Applicant claims priority benefit to the following U.S. application(s):

Application No.: 10/460,044 Filing Date: June 12, 2003 Filing Date:

The Declaration and Filing Fee are NOT ENCLOSED.

[X] Name, Address and Citizenship of Inventor(s) is as follows:

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[X] PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN

[X] Associate this file with Customer No. 27975.

PATENT TRADEMARK OFFICE

Date: <u>July 1, 2003</u>

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